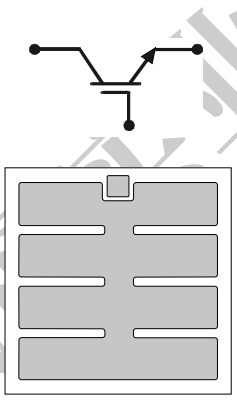
tentative

XPT IGBT Chip



Type	V_{CE} [V]	I_C [A]	Chip Size [mm] x [mm]	Package	Ordering Code
IX137X17A	1700	100	11.7 11.72	sawn on foil ☐	-
				unsawn wafer ☐	-
				in waffle pack ☑	tbd

Features / Advantages:

- Easy paralleling due to the positive temperature coefficient of the on-state voltage
- Rugged XPT design (Xtreme light Punch Through) results in:
  - short circuit rated for 10 µsec.
  - very low gate charge
  - square RBSOA @ 2x Ic
  - low EMI
- Thin wafer technology combined with the XPT design results in a competitive low Vce(sat)

Applications:

- AC motor drives
- Solar inverter
- Medical equipment
- Uninterruptible power supply
- Air-conditioning systems
- Welding equipment

Mechanical Parameters

Parameters	Conditions		Ratings	Unit
Area active			100.7	mm^2
Area total			137.12	mm^2
Wafer size Ø			150	mm
Thickness			190	µm
Material	SiFZ	Orientation	<100>	
Max. possible chips	per wafer			
Passivation	front side		SiN	
Metalization	top side		AlSi	
	backside		Al / Ti / Ni / Ag	
Recom. wire bonds (Al)	Emitter	Number / Ø	12 / 300	- / µm
	Gate	Number / Ø	1 / 300	- / µm
Reject Ink Dot Size	Ø		0.4-1.0	mm
Recom. Storage Environment	in orig. container, in dry nitrogen		< 6	month
	Storage Temperature (Tstg)		-40 ... 40	°C
Soldering temperature (5 min.)			max. tbd	°C
Virtual junction temperature T_{VJ}			-40 ... 150	°C

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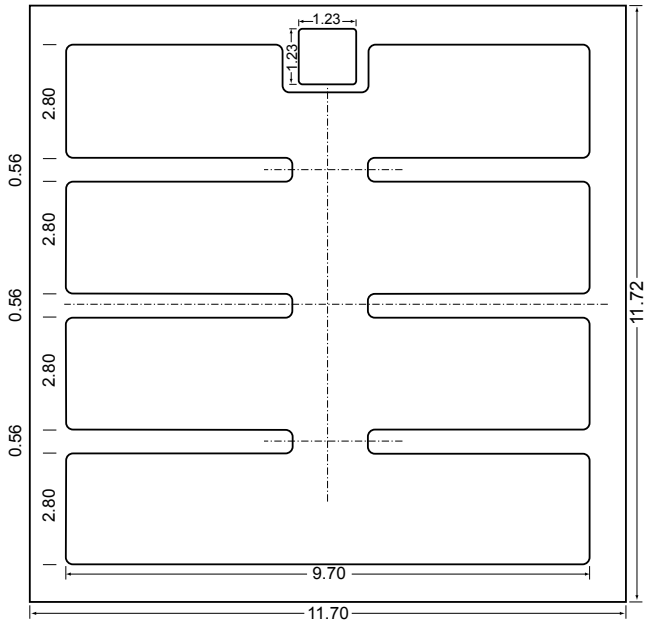
Electrical Parameters

Symbol	Definition	Conditions			Ratings min.	typ.	max.	Unit
V_{CES}	*Collector emitter voltage*	V_{GE} = 0 V	I_C = 1 mA	T_{VJ} = 25°C			1700	V
V_{GES}	*Maximum DC gate voltage*						±20	V
I_C	*Collector current* *(depending on thermal properties of assembly)*						100	A
$V_{CE\ sat}$	*Collector emitter saturation voltage*	V_{GE} = 15 V	I_C = 100 A	T_{VJ} = 25°C		2	2.3	V
				T_{VJ} = 125°C		2.35		V
V_{TO}	*Threshold voltage*	V_{GE} = 15 V;	T_{VJ} = 150 °C				1.1	V
r_T	*(for power loss calculation)*						16.5	m Ω
I_{CES}	*Collector emitter leakage current*	V_{CE} = 1700 V	V_{GE} = 0 V	T_{VJ} = 25°C		10	100	µA
				T_{VJ} = 125°C		100		µA
I_{GES}	*Gate emitter leakage current*	V_{CE} = 0 V	V_{GE} = ±20 V				500	nA
$V_{GE(th)}$	*Gate emitter threshold voltage*	I_C = 4 mA	V_{CE} = V_{GE}	T_{VJ} = 25°C	5.5		6.5	V
Q_{Gon}	*Total gate charge*	I_C = 100 A	V_{CE} = 900 V	V_{GE} = 15 V		310		nC
$t_{d(on)}$	*Turn-on delay time*					130		ns
t_r	*Current rise time*					90		ns
$t_{d(off)}$	*Turn-off delay time*	V_G = 900 V	I_C = 100 A			400		ns
t_f	*Current fall time*	R_G = 6.8 Ω	V_{GE} = ±15 V	T_{VJ} = 125°C		150		ns
E_{on}	*Turn-on energy per pulse*	measured with: DWHP 68-18				25		mJ
E_{off}	*Turn-off energy per pulse*					24		mJ
RBSOA	*Reverse bias safe operation area*	V_{GE} = 15 V	R_G = 6.8 Ω	T_{VJ} = 125°C				
				V_{CEK} = 1700 V			200	A
SCSOA	*Short circuit safe operation area*							
t_{SC}	*Short circuit duration*	V_{CE} = 1300 V	V_{GE} = ±15 V	T_{VJ} = 125°C			10	µs
I_{SC}	*Short circuit current*	R_G = 6.8 Ω	non-repetitive			300		A

Data according to IEC 60747

Dimensions (1 mm = 0.0394")



A [mm]	B [mm]	C [mm]	D [mm]	E [mm]
11.7	11.72	n/a	n/a	n/a

F [mm]	G [mm]	H [mm]	I [mm]	J [mm]
n/a	n/a	n/a	n/a	n/a

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